L Number	Hits	Search Text	DB	Time stamp
-	19	(motherboard and computer and (cpu microprocessor	USPAT;	2003/04/17 21:20
		processor) and dsp and memory) and ((spoken voice	US-PGPUB;	
		speech) adj1 command)	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	33	(motherboard and computer and (cpu microprocessor	USPAT;	2003/04/17 21:20
		processor) and dsp and memory) and ((spoken voice speech	US-PGPUB;	
		audio) adj1 command)	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	16	(motherboard and computer and (cpu microprocessor	USPAT;	2003/04/17 21:21
		processor) and dsp and memory) and ((spoken voice speech	US-PGPUB;	
		audio) adj1 command) and pci	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	13	(motherboard and computer and (cpu microprocessor	USPAT;	2003/04/17 21:24
		processor) and dsp and memory) and ((spoken voice speech	US-PGPUB;	
		audio) adj1 command) and pci and bus and power and input	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	3	(motherboard and computer and (cpu microprocessor	USPAT;	2003/04/17 21:24
		processor) and dsp and memory) and ((spoken voice	US-PGPUB;	
		speech) adj1 command) and pci and bus and power and	EPO; JPO;	
İ		input	DERWENT;	
			IBM_TDB	
-	38	(motherboard PCB) and computer and (cpu microprocessor	USPAT;	2003/04/17 21:56
		processor) and dsp and memory and ((spoken speech verbal	US-PGPUB;	
		voice audio) adj1 command)	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	11	((motherboard PCB) and computer and (cpu microprocessor	USPAT;	2003/04/17 22:00
		processor) and dsp and memory and ((spoken speech verbal	US-PGPUB;	
		voice audio) adj1 command)) and 704/\$.ccls.	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	10	((motherboard PCB) and computer and (cpu microprocessor	USPAT;	2003/04/17 21:54
		processor) and dsp and memory and ((spoken speech verbal	US-PGPUB;	
		voice audio) adj1 command)) and 704/\$.ccls. and bus	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	13	((motherboard PCB) and computer and (cpu microprocessor	USPAT;	2003/04/17 21:52
		processor) and dsp and memory and ((spoken speech verbal	US-PGPUB;	
		voice audio) adj1 command)) and pci and power	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	47	(motherboard PCB (main adj1 circuit adj1 board) backplane)	USPAT;	2003/04/17 21:58
		and computer and (cpu microprocessor processor) and dsp	US-PGPUB;	
		and memory and ((spoken speech verbal voice audio) adj1	EPO; JPO;	
		command)	DERWENT;	
			IBM_TDB	
-	50	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 16:02
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
1		microprocessor processor) and dsp and memory and	EPO; JPO;	
1		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
ŀ		704/\$.ccls.	IBM_TDB	
-	1	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/17 22:02
İ		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls. and pci	IBM_TDB	

-	24	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/17 22:10
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and pci	DERWENT;	
		and (memory same dsp)	IBM_TDB	
_	20	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 20:54
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	2000,0 1, 10 20.04
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and pci	DERWENT;	
	20	and (memory same dsp) and power	IBM_TDB	0000/04/47 00:44
-	32	(((motherboard mother-board (mother board) PCB (main	USPAT;	2003/04/17 22:11
		adj1 circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.) and (memory same dsp)	IBM_TDB	
-	23	(((motherboard mother-board (mother board) PCB (main	USPAT;	2003/04/17 22:19
		adj1 circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.) and bus and (memory same dsp) and power	IBM TDB	
-	1	"05844824"	USPAT;	2003/04/17 22:19
	'	00011021	US-PGPUB;	2000,0 1,7 1,7 22.10
			EPO; JPO;	
			DERWENT;	
	0.4	/// the adversaria the control of the contr	IBM_TDB	0000/04/47 00:04
-	24	(((motherboard mother-board (mother board) PCB (main	USPAT;	2003/04/17 22:21
		adj1 circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and	DERWENT;	
		704/\$.ccls.) and bus and (memory same dsp)	IBM_TDB	
-	50	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 10:41
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and	DERWENT;	
		(bus pci)) and pci	IBM_TDB	
_	34	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 10:42
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and	DERWENT;	
		(bus pci)) and pci and speech	IBM_TDB	2002/04/49 46:00
-	29	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 16:06
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and	DERWENT;	
		(bus pci)) and pci and (speech adj1 recognit\$4)	IBM_TDB	
-	20	((motherboard mother-board (mother board) PCB (main adj1	USPAT;	2003/04/18 15:57
		circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and	DERWENT;	
		(bus pci)) and pci and (speech adj1 recognit\$4) and power	IBM_TDB	
_	19	(((motherboard mother-board (mother board) PCB (main	USPAT;	2003/04/18 16:05
	'-	adj1 circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
		microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and	DERWENT:	
		(spoken speech verbal voice addity add command) and (bus pci)) and pci and (speech adj1 recognit\$4) and power)	IBM_TDB	
			םטו_ואמו	
		and dynamic\$5 and command	LICDAT	2002/04/40 40:00
-	1	(((motherboard mother-board (mother board) PCB (main	USPAT;	2003/04/18 16:00
		adj1 circuit adj1 board) backplane) and computer and (cpu	US-PGPUB;	
	ł	microprocessor processor) and dsp and memory and	EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command) and	DERWENT;	
		((spoken speech verbal voice audio) adj1 command) and (bus pci)) and pci and (speech adj1 recognit\$4) and power) and dynamic\$5 and command and phoneme	DERWENT; IBM_TDB	

-	50	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and	USPAT; US-PGPUB; EPO; JPO;	2003/04/18 16:15
		((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.	DERWENT; IBM_TDB	
-	42	(((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu	USPAT; US-PGPUB;	2003/04/18 16:17
		microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and	EPO; JPO; DERWENT;	
		704/\$.ccls.) and vocabulary and ((speech voice) adj1 recognition)	IBM_TDB	
-	29	((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu	USPAT; US-PGPUB;	2003/04/18 16:15
		microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command) and	EPO; JPO; DERWENT;	
_	32	(bus pci)) and pci and (speech adj1 recogni\$4) (((motherboard mother-board (mother board) PCB (main	IBM_TDB USPAT;	2003/04/18 16:18
		adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and	US-PGPUB; EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and vocabulary and ((speech voice) adj1	DERWENT; IBM_TDB	
-	11	recognition) and bus (((motherboard mother-board mother board) PCB (main	USPAT;	2003/04/18 18:19
		adj1 circuit adj1 board) backplane) and computer and (cpu microprocessor processor) and dsp and memory and	US-PGPUB; EPO; JPO;	
		((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and vocabulary and ((speech voice) adj1	DERWENT; IBM_TDB	
		recognition) and bus and phoneme and (continuous adj1 speech) and (voice adj1 command)		
-	20	(((motherboard mother-board (mother board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu	USPAT; US-PGPUB;	2003/04/18 20:50
		microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and	EPO; JPO; DERWENT;	
		704/\$.ccls.) and vocabulary and ((speech voice) adj1 recognition) and bus and phoneme	IBM_TDB	
-	6	"06154830" "06085314"	USPAT; US-PGPUB;	2003/04/18 18:24
			EPO; JPO; DERWENT;	
-	2	"06170049"	IBM_TDB USPAT;	2003/04/18 18:24
			US-PGPUB; EPO; JPO;	
	00	(((magh) and again magh) are has and (magh) to and (DOD) and	DERWENT; IBM_TDB	2002/04/42 02:52
-	20	(((motherboard mother-board (mother board) PCB) and computer and (cpu microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1	USPAT; US-PGPUB;	2003/04/18 20:53
		command)) and 704/\$.ccls.) and vocabulary and ((speech voice) adj1 recognition) and bus and phoneme	EPO; JPO; DERWENT;	
-	10	(((motherboard mother-board) and computer and (cpu microprocessor processor) and dsp and memory and	IBM_TDB USPAT; US-PGPUB;	2003/04/18 20:51
		((spoken speech verbal voice audio) adj1 command)) and 704/\$.ccls.) and vocabulary and ((speech voice) adj1	EPO; JPO; DERWENT;	
	20	recognition) and bus and phoneme ((((mother board) PCB) and computer and (cpu	IBM_TDB USPAT;	2003/04/18 20:53
		microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and	US-PGPUB; EPO; JPO;	
		704/\$.ccls.) and vocabulary and ((speech voice) adj1 recognition) and bus and phoneme	DERWENT; IBM_TDB	
-	11	((motherboard mother-board (mother adj1 board) PCB (main adj1 circuit adj1 board) backplane) and computer and (cpu	USPAT; US-PGPUB;	2003/04/18 20:55
		microprocessor processor) and dsp and memory and ((spoken speech verbal voice audio) adj1 command)) and pci	EPO; JPO; DERWENT;	
		and (memory same dsp) and power	IBM_TDB	

-	87	PCI and PDA and dsp and cpu	USPAT; US-PGPUB	2003/04/18 21:28
-	36	PCI and PDA and dsp and cpu and ((speech voice) adj1 recognition)	USPAT; US-PGPUB	2003/04/18 22:42
-	9	PCI and ((cellular mobile) adj1 phone) and (speech adj1 recognition) and dsp	USPAT; US-PGPUB	2003/04/18 22:55
-	3	PCI and ((cellular mobile) adj1 phone) and vocabulary	USPAT;	2003/04/18 22:56
-	46	704/\$.ccls. and ((mobile (phone telephone)) or (cellular phone)) and DSP and (noise adj1 cancel\$6)	US-PGPUB USPAT; US-PGPUB;	2003/04/19 20:11
!			EPO; JPO; DERWENT; IBM_TDB	
-	4	(dsp (dsp adj1 chip)) and (menu near1 select\$) and (voice adj1 dialing)	USPAT; US-PGPUB	2003/04/19 21:03
-	7	(dsp (dsp adj1 chip)) and (menu near1 select\$) and (voice adj1 dialing)	USPAT; US-PGPUB;	2003/04/19 21:05
			EPO; JPO; DERWENT; IBM_TDB	
-	0	(menu near1 select\$) and (voice adj1 dialing) and (noise adj1 cancellation)	USPAT; US-PGPUB; EPO; JPO;	2003/04/19 21:29
	_		DERWENT; IBM_TDB	0000/04/40 00:54
-	5	(voice adj1 dialing) and (dsp (dsp adj1 chip)) and (menu adj1 select\$)	USPAT; US-PGPUB; EPO; JPO;	2003/04/19 22:54
	5	(reice edit concel\$5) and (den (den edit chin)) and (manu	DERWENT; IBM_TDB USPAT;	2003/04/19 23:05
-	3	(noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu adj1 select\$)	US-PGPUB; EPO; JPO;	2000/04/19 23:03
-	13	(noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (menu)	DERWENT; IBM_TDB USPAT;	2003/04/19 23:10
			US-PGPUB; EPO; JPO; DERWENT;	
-	20	(noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech	IBM_TDB USPAT;	2003/04/19 23:14
		adj1 recognition)	US-PGPUB; EPO; JPO; DERWENT;	
-	5	((noise adj1 cancel\$5) and (dsp (dsp adj1 chip)) and (speech	IBM_TDB USPAT;	2003/04/19 23:13
		adj1 recognition)) and (dsp same (speech adj1 recognition))	US-PGPUB; EPO; JPO; DERWENT;	
-	1	(noise adj1 cancel\$5) same (dsp) and (speech adj1 recognition)	IBM_TDB USPAT; US-PGPUB;	2003/04/19 23:14
			EPO; JPO; DERWENT;	
	2	6182203.URPN.	IBM_TDB USPAT	2003/04/26 13:26
]_	2 0	6539467.URPN.	USPAT	2003/04/26 13:28
_	0	6453405.URPN.	USPAT	2003/04/26 13:28
_	17	("4814976" "5072418" "5329471" "55353331" "5617543"	USPAT	2003/04/26 13:28
		"5655098" "5752273" "5864703" "6073228" "6167466" "6182203" "6209082" "6219773" "6260137" "6266686" "6289418" "6349383").PN.		
-	0	704/\$.ccls. and CPU and dsp and (command adj1 mode) and	USPAT;	2003/09/22 23:09
		pda	US-PGPUB	

-	8	704/\$.ccls. and CPU and dsp and (command) and pda	USPAT; US-PGPUB	2003/09/22 22:51
-	5	704/\$.ccls. and CPU and dsp and (command and mode) and pda	USPAT; US-PGPUB	2003/09/22 23:10
-	5	704/\$.ccls. and (CPU (maim adj1 processor)) and dsp and (command and mode) and pda	USPAT; US-PGPUB	2003/09/22 23:12
-	1	704/231.ccls. and (CPU (maim adj1 processor)) and dsp and (command) and pda	USPAT; US-PGPUB	2003/09/22 23:13
-	13	704/\$.ccls. and (CPU (maim adj1 processor)) and dsp and (command) and pda and (speech adj1 recognition)	USPAT; US-PGPUB	2003/09/24 20:37
-	50	704/\$.ccls. and motorola and dsp and (command instruction) and (speech adj1 recognition)	USPAT; US-PGPUB	2003/09/23 10:15
_	1	5873062.URPN.	USPAT	2003/09/23 09:59
-	1	"5640490".PN.	USPAT	2003/09/23 10:00
-	31	5640490.URPN.	USPAT	2003/09/23 10:00
-	25	("3581192" "3703609" "3838217" "3938394" "3940565" "3969972" "4181813" "4452079" "4658252" "4780906" "4817154" "4852170" "4862503" "4975957" "4991216" "4998280" "5027410" "5065432" "5068900" "5091948" "5121434" "5166981" "5202926" "5299125" "5321608").PN.	USPAT	2003/09/23 10:08
-	1	704/\$.ccls. and dedicat\$ near (speech adj1 processor)	USPAT; US-PGPUB	2003/09/23 11:02
-	1	6230138.URPN.	USPAT	2003/09/23 10:18
-	1	6230138.URPN.	USPAT	2003/09/23 10:18
-	42	704/\$.ccls. and (two adj1 processors)	USPAT; US-PGPUB	2003/09/23 11:03
-	21	704/\$.ccls. and (two adj1 processors) and dsp	USPAT; US-PGPUB	2003/09/23 11:36
-	1	704/\$.ccls. and (two adj1 processors) and dsp and pda	USPAT; US-PGPUB	2003/09/23 11:04
-	2	704/\$.ccls. and (two adj1 processors) and pda	USPAT; US-PGPUB	2003/09/23 11:04
-	3	704/\$.ccls. and (two adj1 processors) and pda	USPAT; US-PGPUB	2003/09/23 11:05
-	2	704/\$.ccls. and (two adj1 processors) and pda	USPAT; US-PGPUB	2003/09/23 11:36
-	284	704/\$.ccls. and processors and pda	USPAT; US-PGPUB	2003/09/23 11:37
-	63	704/\$.ccls. and processors and pda and dsp	USPAT; US-PGPUB	2003/09/23 11:37
-	43	704/\$.ccls. and processors and pda and dsp and (speech adj1 recognition) and (command instruction) and pda	USPAT; US-PGPUB	2003/09/24 20:35
]-	23	704/\$.ccis. and processors and pda and dsp and (speech adj1 recognition) and (command instruction) and bus	USPAT; US-PGPUB	2004/02/15 15:17
-	3	704/\$.ccls. and processors and pda and dsp and (speech adj1 recognition) and (command instruction) and bus and cpu	USPAT; US-PGPUB	2003/09/23 11:56
-	70	processors and pda and dsp and (speech adj1 recognition) and (command instruction) and bus and cpu	USPAT; US-PGPUB	2003/09/24 18:15
-	5	processors and pda and dsp and (speech adj1 recognition) and (command instruction) and bus and cpu and (reduce adj2 (power consumption))	USPAT; US-PGPUB	2003/09/23 12:00
-	6	processors and pda and dsp and (speech adj1 recognition) and (command instruction) and bus and cpu and ((power adj1 saving) (reduc\$ adj1 power))	USPAT; US-PGPUB	2004/02/15 14:35
-	11		USPAT; US-PGPUB	2003/09/24 20:35
-	1	704/275.ccls. and (CPU (maim adj1 processor)) and dsp and (command) and pda and (speech adj1 recognition)	USPAT; US-PGPUB	2003/09/24 20:38
-	25	(cpu processors) and (dsp same memory) and (speech adj1 recognition) and (command instruction) and bus and ((power	USPAT; US-PGPUB	2004/02/15 14:37
	L	adj1 saving) (reduc\$ adj1 power))	I	l

recognition and (command instruction) and bus and vocabulary and (power adj1 saving) (reduc\$ adj1 power))					
recognition) and (command instruction) and bus and vocabulary and ((power adj1 saving) (reduc\$ adj1 power))	-	2	(cpu processors) and (dsp same memory) and (speech adi1	USPAT:	2004/02/15 14:38
vocabulary and ((power adj1 saving) (reduc\$ adj1 power)) 1 "05390279" USPAT: USPAT: USPAPDBU USPAT 2004/02/15 9 ("4618984" "4827520" "4994983" "5037406" "5033087" USPAT 2004/02/15 9 ("4618984" "4827520" P.N. 1 "4827520" P.N. 1 "4827520" P.N. 1 "4827520" P.N. 1 "4827520" P.N. 1 "6048: cols. and processors and dsp and (speech adj1 recognition) and (command instruction) and bus and memory and vocabulary and slave and vocabulary and slave and vocabulary and slave and vocabulary and slave and vocabulary and slave and vocabulary and slave and vocabulary and slave and vocabulary and slave and (speech adj1 recognition) and (command instruction) and bus and memory and vocabulary and slave and vocabulary and slave and vocabulary and slave and vocabulary and slave and vocabulary and slave and vocabulary and slave and vocabulary and slave and vocabulary and cpu and motherboard 2020059966 URPN. 20020059966 URPN. 20020059966 URPN. 2004/02/16 20020059966 URPN. 2004/02/16 2004/02/1		_			
1 "05390279" USPAT; US-PGPUB USPAT U				00100	
US-PGPUB US-PGPUB		4		LICDAT	2004/02/15 14:51
39 5390279 URPN 9 "4481984" "4827520" "4994983" "5027406" "5033087" USPAT 2004/02/15 20	-	'	00090219	•	2004/02/13 14.51
"(#618984" "4927520" "4994983" "5027406" "5033087" USPAT		20	5200070 LIDDN		0004/00/45 44.54
"5046099" "531669" "5384892" "5390279").PN.	-				
USPAT 2004/02/15 2004/02/15 130 704/\$.ccls. and processors and dsp and (speech adj1 USPAT	-	9	("4618984" "4827520" "4994983" "5027406" "5033087"	USPAT	2004/02/15 15:04
1 "4827520".PN.				_	
130 704/\$.ccls. and processors and dsp and (speech adj1 recognition) and (command instruction) and bus and memory and vocabulary 10 10 10 10 10 10 10 1	-	39			2004/02/15 15:05
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